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Complete if Known

Application Number	09/965,757
Filing Date	September 28, 2001
First Named Inventor	Youfeng Wu
Group Art Unit	2122
Examiner Name	Unassigned
Attorney Docket Number	42390P10792

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	<i>Andr. N. Walker</i>	Date Considered	7/29/04
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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
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Sheet	2	of	2		

OTHER ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
AKJ	C	PATH PROFILE GUIDED PARTIAL DEAD CODE ELIMINATION USING PREDICATION, by RAJIV GUPTA, et al., PACT '97, November 11-15 1997 in San Francisco, California	
AKJ	D	COMPILER-DIRECTED DYNAMIC COMPUTATION REUSE: RATIONALE AND INITIAL RESULTS, by DANIEL A. CONNORS, et al., Department of Electrical and Computer Engineering Center for Reliable and High-Performance Computing, University of Illinois, Urbana, IL 61801, Nov. 1999	
AKJ	E	IMPROVING TRACE CACHE EFFECTIVENESS WITH BRANCH PROMOTION AND TRACE PACKING, by SANJAY JERAM PATEL, et al., Advanced Computer Architecture Laboratory, Department of Electrical Engineering and Computer Science, The University of Michigan, Ann Arbor, Michigan 48109-2122	
AKJ	F	THE SUPERBLOCK: AN EFFECTIVE TECHNIQUE FOR VLIW AND SUPERSCALAR COMPILATION, by WEN-MEI W. HWU, et al., Center for Reliable and High-Performance Computing, University of Illinois, Urbana-Champaign, Illinois, 61801.	
AKJ	G	REPLAY: A HARDWARE FRAMEWORK FOR DYNAMIC PROGRAM OPTIMIZATION, by SANJAY J. PATEL, et al., Center for Reliable and High-Performance Computing, The University of Illinois at Urbana-Champaign, CRHC Technical Report # CRHC-99-16, December 1999	

Examiner Signature <i>Andre T. Fowler</i>	Date Considered <i>7/29/04</i>
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